

Electronic Acknowledgement Receipt

EFS ID:	5185621
Application Number:	10586305
International Application Number:	
Confirmation Number:	2516
Title of Invention:	MANUFACTURING METHOD OF A SEMICONDUCTOR DEVICE HAVING ELECTRODES ON OPPOSED SIDES OF AN IC ELEMENT THEREOF AND FIRST AND SECOND CIRCUIT LAYERS ELECTRICALLY CONNECTED TO THE ELECTRODES
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Attorney Docket Number:	1204.46401X00
Receipt Date:	20-APR-2009
Filing Date:	14-JUL-2006
Time Stamp:	18:45:24
Application Type:	U.S. National Stage under 35 USC 371

Payment information:

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Payment Type	Credit Card
Payment was successfully received in RAM	\$1300
RAM confirmation Number	4289
Deposit Account	
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Request for Continued Examination (RCE)	46401X00-RCE.pdf	132441 120ac662c6980187388a069ce9f001e1b7497	no	1
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2	Amendment Submitted/Entered with Filing of CPA/RCE	46401X00-114Sub-Amd.pdf	98323 e8b1b69705233ecd9a12a9a6523cd13473c93ac3	no	19
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3	Extension of Time	46401X00-2MoEOT.pdf	135177 347625c1f7d548f1d3872e3c27050a3c02f88f	no	1
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4	Transmittal Letter	46401X00-114Sub-IDS.pdf	73566 1c86c8977929758bbaw685938814939450c880b	no	2
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5	Information Disclosure Statement (IDS) Filed (SB/08)	46401X00-FormPTOSB08A.pdf	167095 1162a4894775d76d67e5526295dc5827a35d8	no	1
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6	NPL Documents	46401X00-Supp-ESR.pdf	99339 ec109b7ee022ba5a78a404f1e8b1b803e19b09c3	no	3
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7	Foreign Reference	46401X00-ForRef-1.pdf	1660664 03014c3c0bc0b5e08141a695da05c566a0fda643	no	24
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8	Foreign Reference	46401X00-ForRef-2.pdf	1191336 Bulk1301523c798822780bcac80724304a550a6	no	28
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9	Foreign Reference	46401X00-ForRef-3.pdf	680364 9b11563e58ae883d57b57e671850625d8263	no	9
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10	Foreign Reference	46401X00-ForRef-4.pdf	1044067 56e3ac51594b84f998ced40b445db68368ff	no	12
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11	Fee Worksheet (PTO-06)	fee-info.pdf	32798 36122719ff1124b68461e4b78f8f85c1c27a8ff	no	2
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